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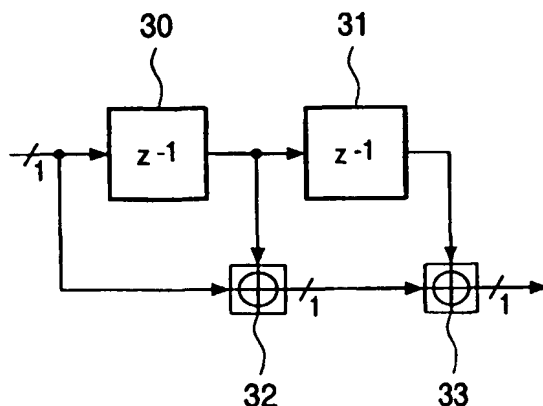
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(54) Title: METHOD FOR GENERATING I/Q SIGNAL IN A TDMA TRANSMITTER AND CORRESPONDING MODULATOR



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(57) Abstract: The present invention relates to a digital I/Q modulator which efficiently supports multi-time-slot operation of wireless TDMA transmitters employing linear power amplifiers. According to the present invention, dips are introduced in the envelope of the I/Q signal in the guard interval between adjacent time-slots or bursts. The dips avoid interference on adjacent radio frequency channels when the gain of the TX chain is switched abruptly in order to change the power level of the TX signal or when the modulation scheme is changed. Also, a method is provided for generating the dips, which is particularly attractive if the modulation scheme in adjacent time-slots changes from GMSK to 8PSK or vice versa.

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